

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
**(Under 37 CFR 1.97(b) or 1.97(c))**

Docket No.  
OKI.302

In Re Application Of: **Tae Yamane**

#2 105  
4-2-02  
2/1/02

Serial No.  
NEW

Filing Date  
FEB. 5, 2002

Examiner  
TO BE ASSIGNED

Group Art Unit  
TO BE ASSIGNED

Title: **CHIP-SIZE SEMICONDUCTOR PACKAGE**

Address to:

**Assistant Commissioner for Patents  
Washington, D.C. 20231**

**37 CFR 1.97(b)**

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

**37 CFR 1.97(c)**

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

**OR**

☐ the fee set forth in 37 CFR 1.17(p).

JC868 U.S. PTO

10/062426



02/05/02

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CHIP-SIZE SEMICONDUCTOR PACKAGE

JC858 U.S. PTO  
10/062426  
02/05/02

**Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of \_\_\_\_\_ is attached.
- ☐ The Assistant Commissioner is hereby authorized to charge and credit Deposit Account No. \_\_\_\_\_ as described below. A duplicate copy of this sheet is enclosed.
- ☐ Charge the amount of \_\_\_\_\_
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Dated: FEB. 5, 2002

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